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09/22/2006

STEVENS DAVIS MILLER & MOSHER, LLP
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WASHINGTON, DC 20036

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APPLICATION NO.	FILING DATE	FIRST NAMED INVENTOR	ATTORNEY DOCKET NO.	CONFIRMATION NO.
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10/804,138

03/19/2004

Osamu Ichikawa

L8462.04111

6381

TITLE OF INVENTION: SEMICONDUCTOR INTEGRATED CIRCUIT AND TEST METHOD THEREOF

APPLN. TYPE	SMALL ENTITY	ISSUE FEE DUE	PUBLICATION FEE DUE	PREV. PAID ISSUE FEE	TOTAL FEE(S) DUE	DATE DUE
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nonprovisional

NO

\$1400

\$300

\$0

\$1700

12/22/2006

EXAMINER	ART UNIT	CLASS-SUBCLASS
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BRITT, CYNTHIA H

2138

714-710000

1. Change of correspondence address or indication of "Fee Address" (37 CFR 1.363).

- ☐ Change of correspondence address (or Change of Correspondence Address form PTO/SB/122) attached.
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2. For printing on the patent front page, list

- (1) the names of up to 3 registered patent attorneys or agents OR, alternatively,
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3. ASSIGNEE NAME AND RESIDENCE DATA TO BE PRINTED ON THE PATENT (print or type)

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(A) NAME OF ASSIGNEE

(B) RESIDENCE: (CITY AND STATE OR COUNTRY) Osaka, JAPAN 1420.00 OP
309.00 OP

MATSUSHITA-ELECTRIC INDUSTRIAL CO., LTD.

Osaka, JAPAN

FC:1501

62 FC:1504

1420.00 OP

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Please check the appropriate assignee category or categories (will not be printed on the patent): ☐ Individual ☒ Corporation or other private group entity ☐ Government

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James E. Ledbetter

Date November 16, 2006

Registration No. 28,732

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